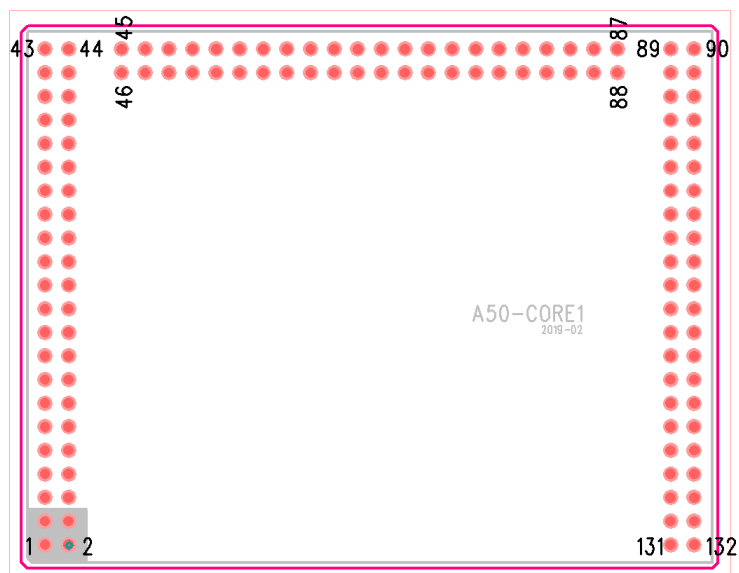
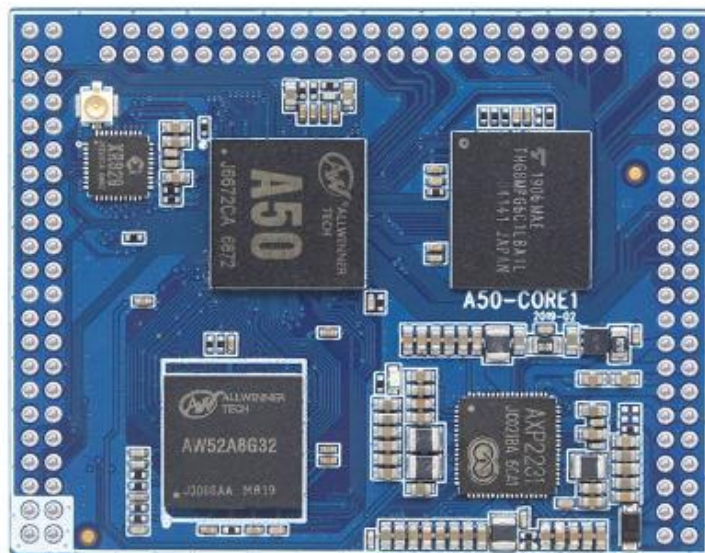


A50-Core 管脚定义

A50-Core 核心板接口采用三组 2.0 双排插针形式，共 132 个引脚，尺寸：**59mm*46mm**



引脚定义表:

(用户可以使用硬件资料里的核心板插座封装原理图和 PCB 做底板设计)

No.	Net Name	No.	Net Name
1	PB0/UART2-TX	2	PB1/UART2-RX
3	PB2/UART2-RTS/EINT2	4	PB3/UART2-CTS/EINT3
5	PB4/I2S0-LRCK/EINT4	6	PB5/I2S0-BCLK/EINT5
7	PB6/I2S0-DOOUT/EINT6	8	PB7/I2S0-DIN/EINT7
9	PB8/I2S0-MCLK/EINT8	10	PB9/UART0-TX
11	PB10/UART0-RX	12	GND
13	GND	14	PF1/SDC0-D0
15	PF0/SDC0-D1	16	PF3/SDC0-CMD
17	PF2/SDC0-CLK	18	PF5/SDC0-D2
19	PF4/SDC0-D3	20	PF6/SDC0-DET
21	GND	22	GND
23	PH12/EINT12/4G-SW	24	PH11/EINT11/TP-INT
25	PH10/EINT10/TP-RST	26	PH9/EINT9/LCD-RST
27	PH8/EINT8/USB-ID	28	PH7/UART3-CTS/SPI1-MISO
29	PH6/UART3-RTS/SPI1-MOSI	30	PH5/UART3-RX/SPI1-CLK
31	PH4/UART3-TX/SPI1-CS	32	PH3/TWI1-SDA
33	PH2/TWI1-SCK	34	PH1/TWI0-SDA
35	PH0/TWI0-SCK	36	GND
37	GND	38	GND
39	USB1-DP	40	USB1-DM
41	USB0-DP	42	USB0-DM
43	GND	44	GND
45	AGND	46	MIC3P-MIC
47	MIC2P-MIC	48	HPOUTR
49	HPOUTL	50	HP-DET
51	GND	52	GND

53	FEL	54	GPADC
55	PE0/MCSI-MCLK	56	PE1/MCSI-SCK
57	PE2/MCSI-SDA	58	PE3/MCSI-STBY-F
59	PE4/MCSI-RST-R/F	60	PE5/MCSI-STBY-R
61	GND	62	GND
63	MCSIA-D3P	64	MCSIA-D3N
65	MCSIA-D2P	66	MCSIA-D2N
67	MCSIA-CLKP	68	MCSIA-CLKN
69	MCSIA-D1P	70	MCSIA-D1N
71	MCSIA-D0P	72	MCSIA-D0N
73	GND	74	GND
75	LCD-BL-EN	76	LCD-PWM
77	LCD-HSYNC	78	LCD-VSYNC
79	LCD-CLK	80	LCD-DE
81	LCD-D22	82	LCD-D23
83	LCD-D20	84	LCD-D21
85	LCD-D18	86	LCD-D19
87	LCD-D14	88	LCD-D15
89	GND	90	GND
91	LCD-D13	92	LCD-D12
93	LCD-D11	94	LCD-D10
95	LCD-D6	96	LCD-D7
97	LCD-D4	98	LCD-D5
99	LCD-D2	100	LCD-D3
101	GND	102	GND
103	MIPI-DSI-D0P	104	MIPI-DSI-D0N
105	MIPI-DSI-D1P	106	MIPI-DSI-D1N
107	MIPI-DSI-CKP	108	MIPI-DSI-CKN
109	MIPI-DSI-D2P	110	MIPI-DSI-D2N

111	MIPI-DSI-D3P	112	MIPI-DSI-D3N
113	GND	114	GND
115	VBAT	116	VBAT
117	DC5V	118	DC5V
119	PS	120	PS
121	USBVBUS	122	USBVBUS
123	CHGLED	124	AP-RESET
125	USB0-DRVVBUS	126	PWRON
127	AVDD-CSI	128	VCC-3V3
129	DVDD-CSI	130	VCC-LCD
131	DOVDD-CSI	132	VIN-RTC